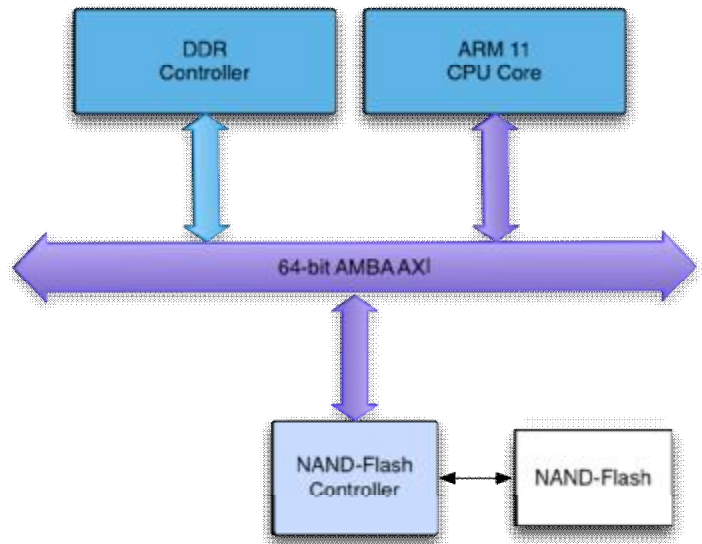


IP Core NAND Flash Controller

Key Features

- DMA-Master NAND Flash memory controller
- 8-bit NAND-Flash interface
- Hardware checksum calculation
- Several memory pages transfer per transaction
- Minimal CPU overhead
- Linux MTD driver



Technical characteristics

- Specially designed for high performance with MTD Drivers Linux subsystem
- Support for 8-bit NAND-Flash chips
- Reading speed up to 40 MB/sec
- Hardware checksum calculation
- Asynchronous DMA-Master interface allows CPU context switch during transaction, reducing CPU load
- Bulk read operations are supported to reduce number of interrupts

List of Deliverables

- Verilog source code
- Verilog test bench and Verification environment
- Software driver for Linux OS
- Example synthesis scripts
- Documentation

Interfaces

- 8-bit parallel NAND-Flash interface
- 32-bit AMBA AXI Master
- APB Slave for configuration
- Linux MTD drivers are available

Size

- 50K gates (w/o memory)

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